

May 1998

# **DS3487 Quad TRI-STATE® Line Driver**

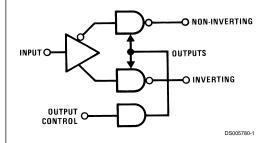
## **General Description**

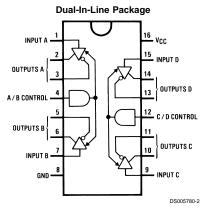
National's quad RS-422 driver features four independent drivers which comply with EIA Standards for the electrical characteristics of balanced voltage digital interface circuits. The outputs are TRI-STATE structures which are forced to a high impedance state when the appropriate output control pin reaches a logic zero condition. All input pins are PNP buffered to minimize input loading for either logic one or logic zero inputs.

#### **Features**

- Four independent drivers
- TRI-STATE ® outputs
- Fast propagation times (typ 10 ns)
- TTL compatible
- 5V supply
- Output rise and fall times less than 15 ns
- Pin compatible with DS8924 and MC3487

## **Block and Connection Diagrams**





**Top View** Order Number DS3487M or DS3487N See NS Package Number M16A or N16E

#### **Truth Table**

Input	Control	Non-Inverting	Inverting
	Input	Output	Output
Н	Н	Н	L
L	Н	L	Н
X	L	Z	Z

- L = Low logic state
- H = High logic state
- X = Irrelevant Z = TRI-STATE(high impedance)

TRI-STATE® is a registered trademark of National Semiconductor Corporation.

### **Absolute Maximum Ratings** (Note 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage 8V 5.5V Input Voltage -65°C to +150°C Storage Temperature

Maximum Power Dissipation (Note 1) at 25°C

Molded DIP Package 1476 mW SO Package 1051 mW

Lead Temperature

260°C (Soldering, 4 seconds)

## **Operating Conditions**

	Min	Max	Units
Supply Voltage, V <sub>CC</sub>			
DS3487	4.75	5.25	V
Temperature (T <sub>A</sub> )			
DS3487	0	+70	°C
Note 1: Derate DIP molded packat package 8.41 mW/°C above 25°C.	ge 11.9 mW/°	C above 25°C.	Derate SO

#### Electrical Characteristics (Notes 3, 4, 5, 6)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V <sub>IL</sub>	Input Low Voltage					0.8	V
V <sub>IH</sub>	Input High Voltage			2.0			V
I <sub>IL</sub>	Input Low Current	V <sub>IL</sub> = 0.5V				-200	μA
I <sub>IH</sub>	Input High Current		V <sub>IH</sub> = 2.7V			50	μΑ
			V <sub>IH</sub> = 5.5V			100	μA
V <sub>CL</sub>	Input Clamp Voltage	I <sub>CL</sub> = -18 mA				-1.5	V
V <sub>OL</sub>	Output Low Voltage	I <sub>OL</sub> = 48 mA				0.5	V
V <sub>OH</sub>	Output High Voltage	I <sub>OH</sub> = -20 mA		2.5			V
I <sub>os</sub>	Output Short-Circuit Current			-40		-140	mA
I <sub>oz</sub>	Output Leakage Current		V <sub>O</sub> = 0.5V			-100	μA
	(TRI-STATE)		V <sub>O</sub> = 5.5V			100	μA
I <sub>OFF</sub>	Output Leakage Current Power OFF	V <sub>CC</sub> = 0V	V <sub>O</sub> = 6V			100	μA
			$V_{O} = -0.25V$			-100	μA
V <sub>OS</sub> - V   os	Difference in Output Offset Voltage					0.4	V
V <sub>T</sub>	Differential Output Voltage			2.0			V
V <sub>T</sub>  - V <sub>T</sub>	Difference in Differential Output Voltage					0.4	V
Icc	Power Supply Current		Active		50	80	mA
			TRI-STATE®		35	60	mA

# Switching Characteristics $V_{CC} = 5V$ , $T_A = 25^{\circ}C$

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t <sub>PHL</sub>	Input to Output			10	15	ns
t <sub>PLH</sub>	Input to Output			10	15	ns
t <sub>THL</sub>	Differential Fall Time			10	15	ns
t <sub>TLH</sub>	Differential Rise Time			10	15	ns
t <sub>PHZ</sub>	Enable to Output	$R_L = 200\Omega, C_L = 50 \text{ pF}$		17	25	ns
t <sub>PLZ</sub>	Enable to Output	$R_L = 200\Omega, C_L = 50 \text{ pF}$		15	25	ns
t <sub>PZH</sub>	Enable to Output	R <sub>L</sub> = ∞, C <sub>L</sub> = 50 pF, S1 Open		11	25	ns
t <sub>PZL</sub>	Enable to Output	$R_L = 200\Omega$ , $C_L = 50$ pF, S2 Open		15	25	ns

Note 2: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 3: Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS3487. All typicals are given for  $V_{CC} = 5V$  and  $T_A = 25$ °C.

Note 4: All currents into device pins are positive, all currents out of device pins as negative. All voltages are referenced to ground unless otherwise specified.

Note 5: Only one output at a time should be shorted.

Note 6: Symbols and definitions correspond to EIA RS-422, where applicable.

## **AC Test Circuits and Switching Time Waveforms**

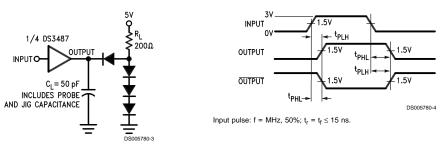
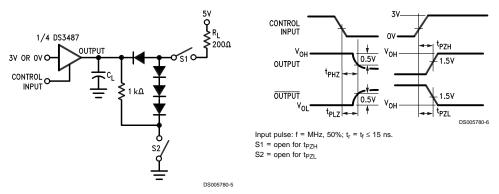


FIGURE 1. Propagation Delays



S1 and S2 closed except as noted. C<sub>L</sub> includes probe and jig capacitance.

FIGURE 2. TRI-STATE Enable and Disable Delays

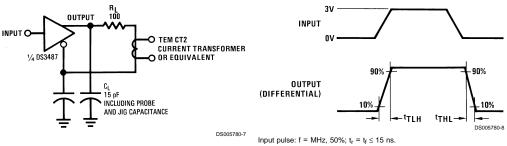
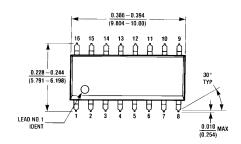
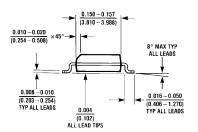
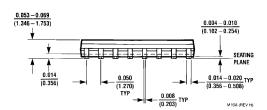


FIGURE 3. Differential Rise and Fall Times

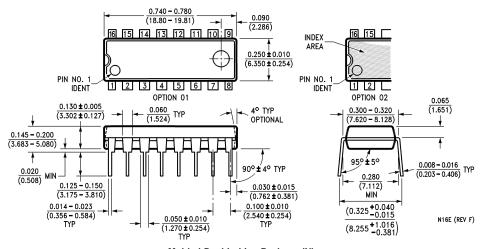
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#### Small Outline Package (M) Order Number DS3487M NS Package Number M16A



Molded Dual-In-Line Package (N) Order Number DS3487N NS Package Number N16E

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